UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

: 7,158,637 B2

Page 1 of 2

APPLICATION NO.: 10/017392

DATED

: January 2, 2007

INVENTOR(S)

: Yuusaku Ohta et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

ON THE TITLE PAGE

In section (56), References Cited, under Other Publications, line 4, please change "System of a Chip" to --System on a Chip--.

In section (57), Abstract please change "A security communication packet processing apparatus (100) comprises an encryption processing unit (102) that performs encryption processing and decryption processing in a data block unit of B1 bits, an authentication processing unit (104) that performs authentication processing in a data block unit of B2 (=n.times.B1) bits in parallel to the encryption processing or the decryption processing in the encryption processing unit (102) and outputs an authentication value, a data block accumulation unit (103) that accumulates the data blocks from the encryption processing unit (102) and outputs the data blocks to the authentication processing unit (104) when the accumulated amount of the data blocks reaches B2 bits, a packet construction unit (105) that reconstructs a packet with the data blocks from the encryption processing unit (102) and the authentication value from the authentication processing unit (104), and an encryption and authentication processing control unit (101) that divides the inputted packet into the data blocks of B1 bits and outputs the data blocks sequentially to the encryption processing unit." to --A security communication packet processing apparatus includes an encryption processing unit that performs encryption and decryption processing in a data block unit of B1 bits, an authentication processing unit that performs authentication processing in a data block unit of B2(= n x B1) bits in parallel to the encryption or decryption processing in the encryption processing unit and outputs an authentication value, a data block accumulation unit that accumulates the data blocks from the encryption processing unit and outputs them to the authentication processing unit when the accumulated data blocks reaches B2 bits, a packet construction unit that reconstructs a packet with the data blocks from the encryption processing unit and the authentication value from the authentication processing unit and a processing control unit that divides the inputted packet into the data blocks of B1 bits and outputs the data blocks sequentially to the encryption processing unit.--.

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CLAIM 2

In column 27, line 64, please change "blocks bits each" to --blocks each--.

CLAIM 18

In column 31, line 17, please change "processing:" to --processing; --.

CLAIM 19

In column 32, line 26, please change "processing and" to --processing, and--.

Signed and Sealed this

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First Day of May, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office